DATA PROCESSING METHOD ON PROCESSOR AND DATA PROCESSING SYSTEM

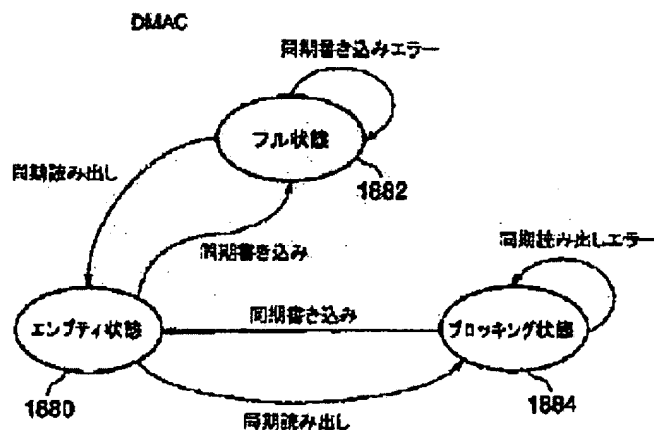|	
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Abstract of JP2002351850

PROBLEM TO BE SOLVED: To provide computer architecture and a programming model for high speed processing via a broadband network. SOLUTION: The above-mentioned architecture uses uniform modular structure, a common computing module and a uniform software cell. A control device, plural of processing units, plural of local memory units as a base on which the processing unit executes a program, a direct memory access, a controller and shared main memory are included in a common computing module. A synchronization system and a method to perform adjusted read and write of data from the shared main memory by the processing unit are provided.